MMBF170LT1

Power MOSFET 500 mA, 60 V N-Channel SOT-23

Features

• Pb–Free Packages are Available

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain–Source Voltage	V _{DSS}	60	Vdc
Drain-Gate Voltage	V _{DGS}	60	Vdc
Gate–Source Voltage – Continuous – Non–repetitive (t _p ≤ 50 μs)	V _{GS} V _{GSM}	±20 ±40	Vdc Vpk
Drain Current – Continuous – Pulsed	I _D I _{DM}	0.5 0.8	Adc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board (Note 1.) T _A = 25°C Derate above 25°C	P _D	225 1.8	mW mW/°C
Thermal Resistance, Junction-to-Ambient	R_{\thetaJA}	556	°C/W
Junction and Storage Temperature	T _J , T _{stg}	–55 to +150	°C

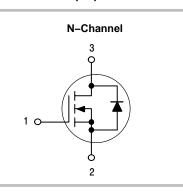
1. FR–5 = 1.0 \times 0.75 \times 0.062 in.



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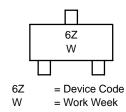
http://onsemi.com

500 mA, 60 V R_{DS(on)} = 5 Ω

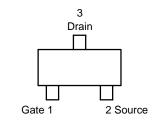




MARKING DIAGRAM



PIN ASSIGNMENT



ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

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ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit
OFF CHARACTERISTICS	3				
Drain–Source Breakdown Voltage (V_{GS} = 0, I_D = 100 μ A)		V _{(BR)DSS}	60	_	Vdc
Gate-Body Leakage Current, Forward (V_{GSF} = 15 Vdc, V_{DS} = 0)		I _{GSS}	-	10	nAdc
ON CHARACTERISTICS	(Note 1)				
Gate Threshold Voltage ($V_{DS} = V_{GS}$, $I_D = 1.0$ mA)		V _{GS(th)}	0.8	3.0	Vdc
Static Drain–Source On–Resistance (V_{GS} = 10 Vdc, I_D = 200 mA)		r _{DS(on)}	-	5.0	Ω
On–State Drain Current (V_{DS} = 25 Vdc, V_{GS} = 0)		I _{D(off)}	-	0.5	μΑ
DYNAMIC CHARACTERI	STICS				
Input Capacitance $(V_{DS} = 10 \text{ Vdc}, V_{GS} = 0 \text{ V}, f = 1.0 \text{ MHz})$		C _{iss}	-	60	pF
SWITCHING CHARACTE	RISTICS (Note 1)	-	-		-
Turn-On Delay Time	$(V_{DD} = 25 \text{ Vdc}, I_D = 500 \text{ mA}, R_{gen} = 50 \Omega)$	t _{d(on)}	-	10	ns
Turn–Off Delay Time	Figure 1	t _{d(off)}	-	10	1

1. Pulse Test: Pulse Width \leq 300 µs, Duty Cycle \leq 2.0%.

ORDERING INFORMATION

Device	Package	Shipping [†]	
MMBF170LT1	SOT-23 (TO-236)	10,000 Tape & Reel	
MMBF170LT1G	SOT-23 (TO-236) (Pb-Free)	3,000 Tape & Reel	
MMBF170LT3	SOT-23 (TO-236)	10,000 Tape & Reel	
MMBF170LT3G	SOT-23 (TO-236) (Pb-Free)	3,000 Tape & Reel	

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

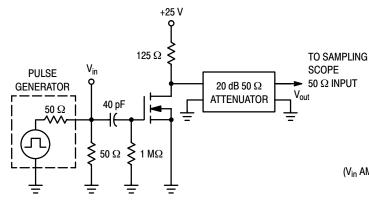


Figure 1. Switching Test Circuit

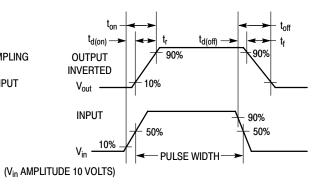
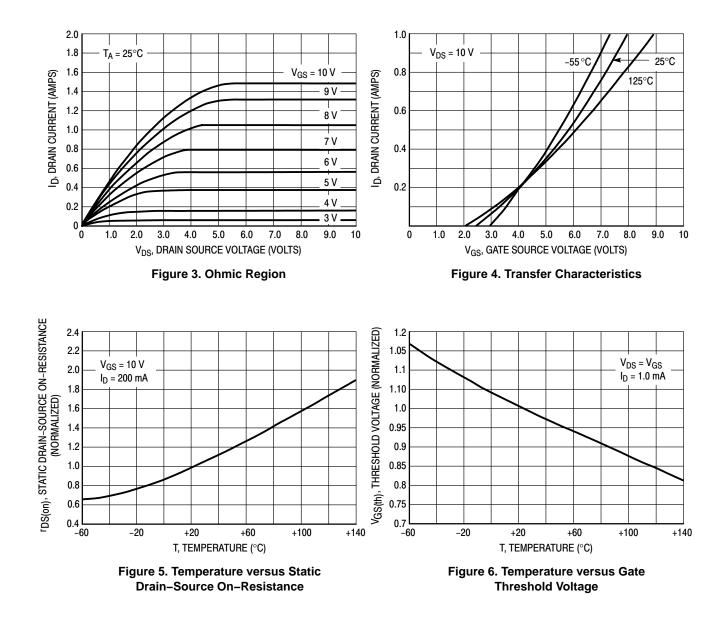


Figure 2. Switching Waveform

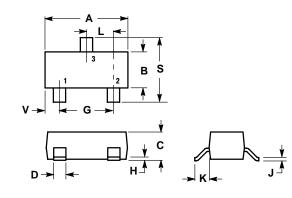
MMBF170LT1

TYPICAL ELECTRICAL CHARACTERISTICS



PACKAGE DIMENSIONS

SOT-23 (TO-236) CASE 318-08 ISSUE AH



NOTES:

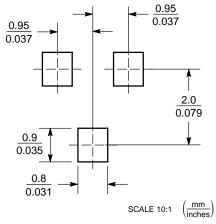
- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: INCH. 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD
- MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL
- 4. 318-03 AND -07 OBSOLETE, NEW STANDARD 318-08.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.1102	0.1197	2.80	3.04
В	0.0472	0.0551	1.20	1.40
С	0.0350	0.0440	0.89	1.11
D	0.0150	0.0200	0.37	0.50
G	0.0701	0.0807	1.78	2.04
Н	0.0005	0.0040	0.013	0.100
J	0.0034	0.0070	0.085	0.177
Κ	0.0140	0.0285	0.35	0.69
L	0.0350	0.0401	0.89	1.02
S	0.0830	0.1039	2.10	2.64
٧	0.0177	0.0236	0.45	0.60



3. DRAIN

SOLDERING FOOTPRINT*



SOT-23

*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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