TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

# T A 8 4 2 8 K

## **1.5 A FULL BRIDGE DRIVER**

The TA8428K is Full Bridge Driver IC for Brush Motor Rotation Control that has current capability of up to 1.5 A (AVE).

Thermal Shutdown and Short Current Protector are provided.

#### **FEATURES**

- 1.5 A (AVE.) full bridge driver
- 4 modes (forward/reverse/short brake and stop) are available with 2 TTL compatible inputs control.
- H-SIP 7 compact SIP package sealed.
- Free wheeling diodes are equipped.
- Multi protection system driver (Thermal shutdown and short current protector)

#### **BLOCK DIAGRAM**



#### 980910EBA2

- 980910EBA
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Weight : 1.88 g (Typ.)

#### **PIN FUNCTION**

PIN No.	SYMBOL	FUNCTIONAL DESCRIPTION
1	IN1	TTL compatible control inputs.
2	IN2	(PNP type low active comparator inputs)
3	OUTA	Output terminals and free wheeling diodes are connected between each output to GND and $V_{CC}$ .
4	GND	GND terminal
5	OUTA	Output terminals and free wheeling diodes are connected between each output to GND and $V_{\mbox{CC}}$ .
6	N.C	Non connection
7	V <sub>CC</sub>	Supply voltage terminal for control and motor drive.

TA8428K has 2 build-in protective functions which work independently. These circuit operations are as follows.



• Thermal shutdown (TSD)

If junction temperature of TA8428K is over the specified temperature (150°C Typ.) by excess power dissipation or abnormal ambient temperature change, thermal Shutdown circuit turn "ON" and output 4 transistors become High impedance. (All transistors turn "OFF")

• Short current protector (ISD)

Short current protector circuit senses all output transistor current. If output transistor current is over the specified limiting current value (3 A Typ.), short current protector operates and all output transistors periodically turn "OFF" (High Impedance Mode) in a period of approximately  $80 \ \mu$ s.

This state is continued until the release of over current mode.



#### **INTERNAL CIRCUIT**



#### **MAXIMUM RATINGS** (Ta = 25°C)

CHARACTERIST	ïC	SYMBOL	RATING	UNIT	
Supply Voltage		Vcc	30	V	
Input Voltage	VIN	-0.3~V <sub>CC</sub>	V		
Output Current	AVE.	<sup>I</sup> O (AVE.)	1.5	А	
Output Current	PEAK	IO (PEAK)	3.0 (Note 1)		
Power Dissipation		1.25 (Note 2)	w		
Fower Dissipation	PD	10.0 (Note 3)			
<b>Operating</b> Temperatu	T <sub>opr</sub>	- 30~85	°C		
Storage Temperature	T <sub>stg</sub>	- 55~150	°C		

(Note 1) t = 100 ms

(Note 2) No heat sink

(Note 3)  $Tc = 85^{\circ}C$ 

### **ELECTRICAL CHARACTERISTICS** ( $V_{CC} = 24 V$ , Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT	
	lCC1		Stop mode	—	8	15	,	
Quiescent Current	ICC2	] 1	Forward / reverse mode	_	35	85	mA	
	ICC3		Brake mode	—	16	30		
Input Voltage	$V_{IL}$	2	_	—	_	0.8	v	
input voltage	VIH	2	—	2.0	_	—	v	
Input Current	ЧЦ	2	V <sub>IN</sub> = GND	—	_	50		
input current	ЧΗ	2	$V_{IN} = V_{CC}$	—	_	10	μA	
Output Saturation Voltage	V <sub>sat</sub> (total)	3	l <sub>O</sub> = 1.5 A, Tc = 25°C	—	2.2	2.9	v	
Output Leakage	ι <sub>LU</sub>	4	)/: _ 2F.)/			50	μA	
Current	ILL	4	V <sub>L</sub> = 25 V	_	—	50		
Diode Forward Voltage	V <sub>FU</sub>	5	5 I <sub>F</sub> = 1.5 A	—	2.6	—	v	
Didde Forward Voltage	V <sub>FL</sub>	5	IF = 1.5 A	—	1.5	—		
Limiting Current	imiting Current I <sub>SD</sub> — —		—	—	3	—	А	
Thermal Shutdown	т <sub>sd</sub>			_	150		°C	
Operating Temperature	- טני –							
Propagation Delay	t <sub>pLH</sub>	2	—	_	1	10		
Time	t <sub>pHL</sub>	2		—	1	10	$\mu$ s	

#### FUNCTION

INP	TUT	OUT	MODE	1	
IN1	IN2	OUTA		– MODE	
Н	Н	L	L	Brake	1
L	Н	L	Н	CW/CCW	
Н	L	Н	L	CCW/CW	1
L	L	OFF (high i	mpedance)	Stop	

- (Note) PIN 6 is non connection.
- (Note) Heat fin is connected with GND with low impedance.

## **TOSHIBA**

#### **TEST CIRCUIT 1.**

ICC1, ICC2, ICC3



#### **TEST CIRCUIT 2.**

VIL, VIH, IIL, IIH, tpLH, tpHL



#### TEST CIRCUIT 3.

 $V_{sat}$ 



## **TOSHIBA**

#### **TEST CIRCUIT 4.**

I<sub>LH</sub>, I<sub>LL</sub>



#### TEST CIRCUIT 5.

V<sub>FU</sub>, V<sub>FL</sub>





#### **APPLICATION CIRCUIT**



(Note) Recommend to take approximately 100  $\mu$ s of input dead time for reliable operations.



- (Note) Utmost care is necessary in the design of the output line, V<sub>CC</sub> and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.
- (Note) In case of mounted on radiators, do not use silicon rubber.

OUTLINE DRAWING HSIP7-P-2.54

Unit : mm



Weight: 1.88 g (Typ.)